

VERSION OF AMENDED CLAIMS WITH
MARKINGS TO SHOW CHANGES MADE

5. (Amended) A [video conferencing] scene detection system comprising [video camera means to generate video] a detector configured to detect a scene over time, a data stream generator in communication with the detector and configured to generate a stream of digital data representing pixels of [a digital] the scene, a first combination memory structure and register to store pixel values representing pixels of said scene at a current frame time, said pixels being bit mapped in said first memory structure and register in accordance with said scene, a second register connected to store pixel values representing said scene at a second frame time prior to said current frame time, said pixel values being bit mapped in said second register in accordance with said scene, a third register connected to store [the] mathematical operation results between the corresponding pixel values in said first register and said second register, wherein said third register stores values representing the temporal changes in said pixel values.

6. (Amended) A [video conferencing] scene detection system as recited in claim 5, further comprising a fourth register connected to store [the] mathematical operation results stored in said third register at a previous frame time to the frame time of the mathematical result values stored in said third register, a fifth register connected to store [the] mathematical results between the corresponding values stored in said fourth register and said fifth register.

7. (Amended) A system as recited in claim [6] 4, further comprising a sixth register for storing mathematical factors corresponding to criteria from a processing algorithm and a flag register connected to store a flag when a value stored in said [fifth] third register exceeds the corresponding factor stored in said sixth register.

8. (Amended) A system for [video conferencing] scene detection as recited in claim [6] 7, further comprising means to transmit [video] digital data representing said scene with a radix control output in accordance [and] with the associated flags stored in said flag register.

9. (Amended) A system for [video conferencing] scene detection as recited in claim [6] 7 further comprising means to transmit [video] digital data representing said scene for only these pixels which meet [the processor set criteria set and stored in said seventh register] criteria set by a processor external to the memory structure.

11. (Amended) A system for video conferencing as recited in claim [11] 5 wherein said [VAM] scene detection system has multiple ports for input and output of memory independently of register and flag signal lines.